
First published in the *Government Gazette*, Electronic Edition, on 5th February 1999 at 5:00 pm.

No. S 50

LAYOUT-DESIGNS OF INTEGRATED CIRCUITS ACT 1999
(ACT 3 OF 1999)

LAYOUT-DESIGNS OF INTEGRATED CIRCUITS ACT
(COMMENCEMENT) NOTIFICATION 1999

In exercise of the powers conferred by section 1 of the Layout-Designs of Integrated Circuits Act 1999, the Minister for Law hereby makes the following Notification:

1. This Notification may be cited as the Layout-Designs of Integrated Circuits Act (Commencement) Notification 1999.
2. The Layout-Designs of Integrated Circuits Act 1999 shall come into operation on 15th February 1999.

Made this 3rd day of February 1999.

GOH KIM LEONG
*Permanent Secretary,
Ministry of Law,
Singapore.*

[LAW 06/011/037]